

10/574065

PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	
Filing Date	Concurrently
First Named Inventor	Theodore Letavic
Art Unit	N/A
Examiner Name	N/A
Attorney Docket Number	US030375

Sheet 1 of 1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/S.H./		US- 6,246,101 B1	06-12-2001	Akiyama	
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/S.H./		US- 6,127,703	10-03-2000	Letavic	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ Number ⁴ Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶

Examiner
Signature

/Shouxiang Hu/

Date
Considered

02/16/2008

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Sheet 1 of 1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/S.H./		"Over 1000V n-ch LDMOSFET and p-ch LIGHT with JI RESURF Structure and Multiple Floating Field Plate" by T. Terashima, Symposium on Power Semiconductor Devices & ICs	

Examiner Signature	/Shouxiang Hu/	Date Considered	02/16/2008
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